

FIG. 1

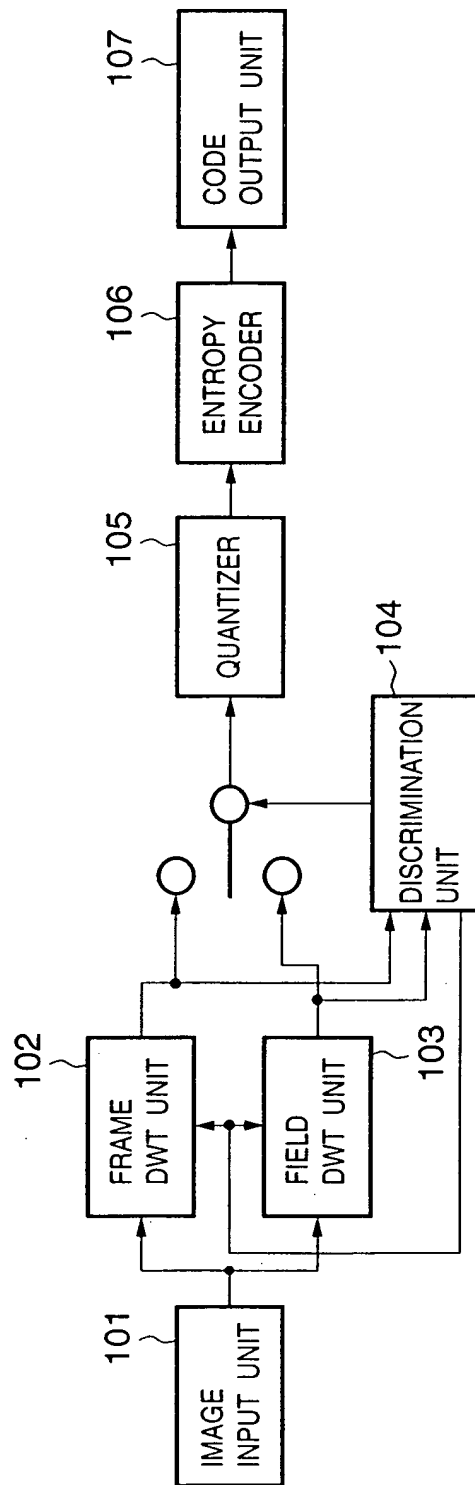


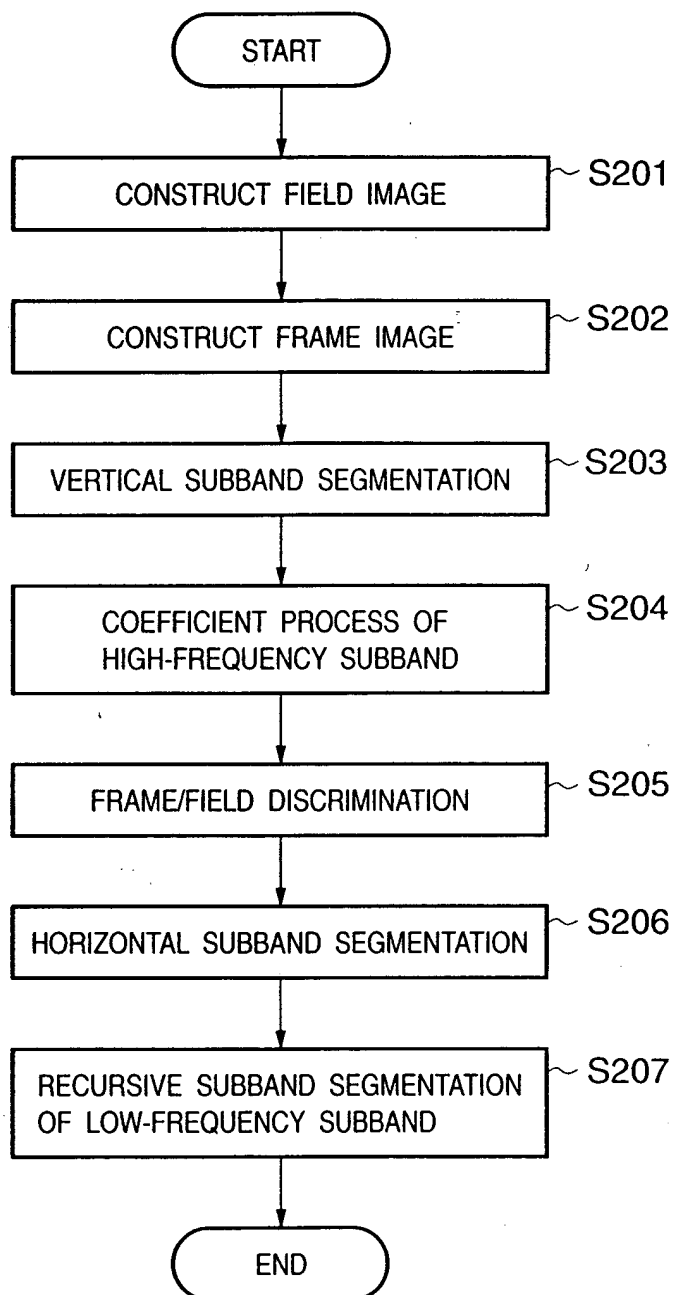
FIG. 2

FIG. 3A

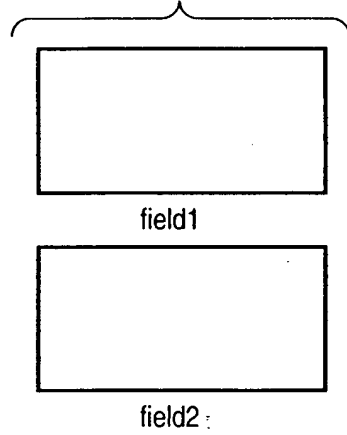


FIG. 3D

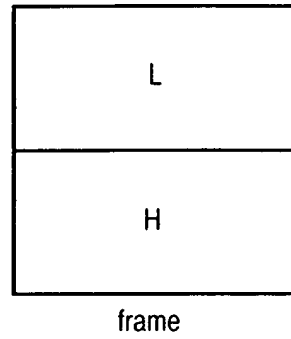


FIG. 3G

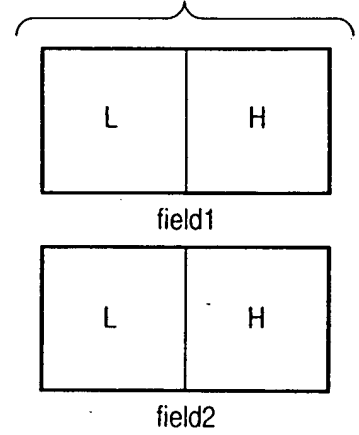


FIG. 3B

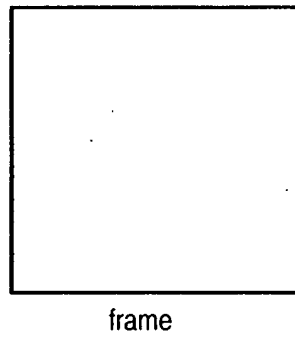


FIG. 3E

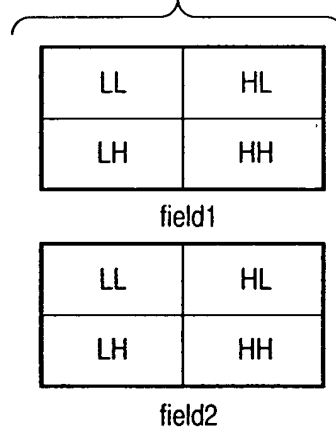


FIG. 3H

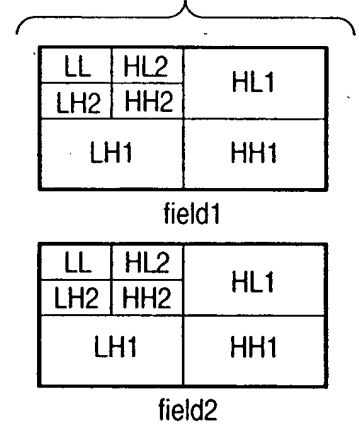


FIG. 3C

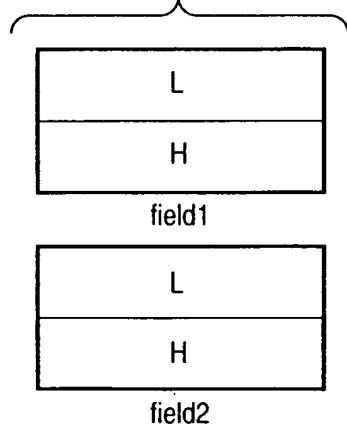


FIG. 3F

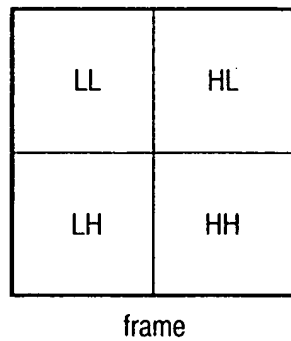


FIG. 3I

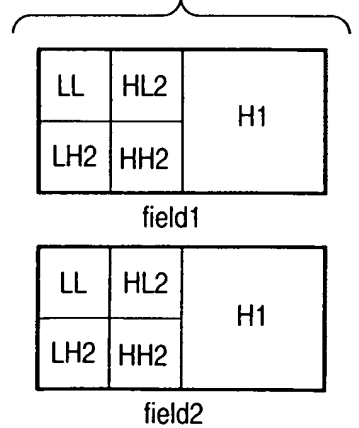


FIG. 3A

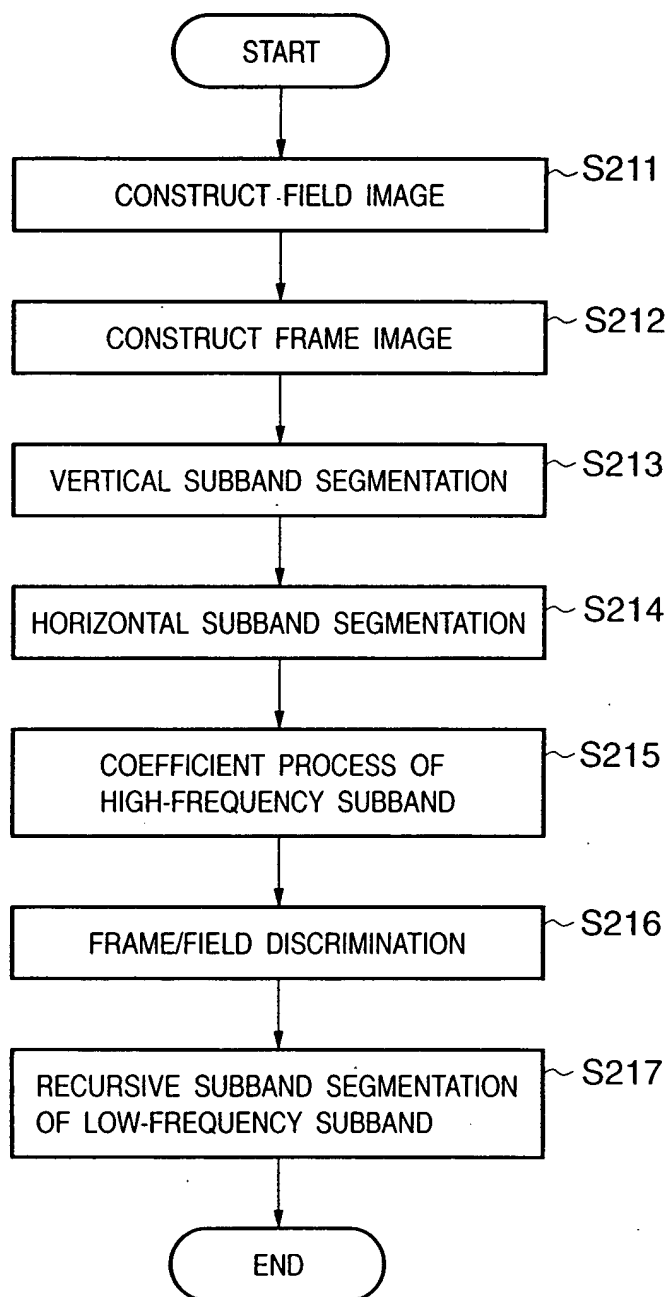
FIG. 4

FIG. 5

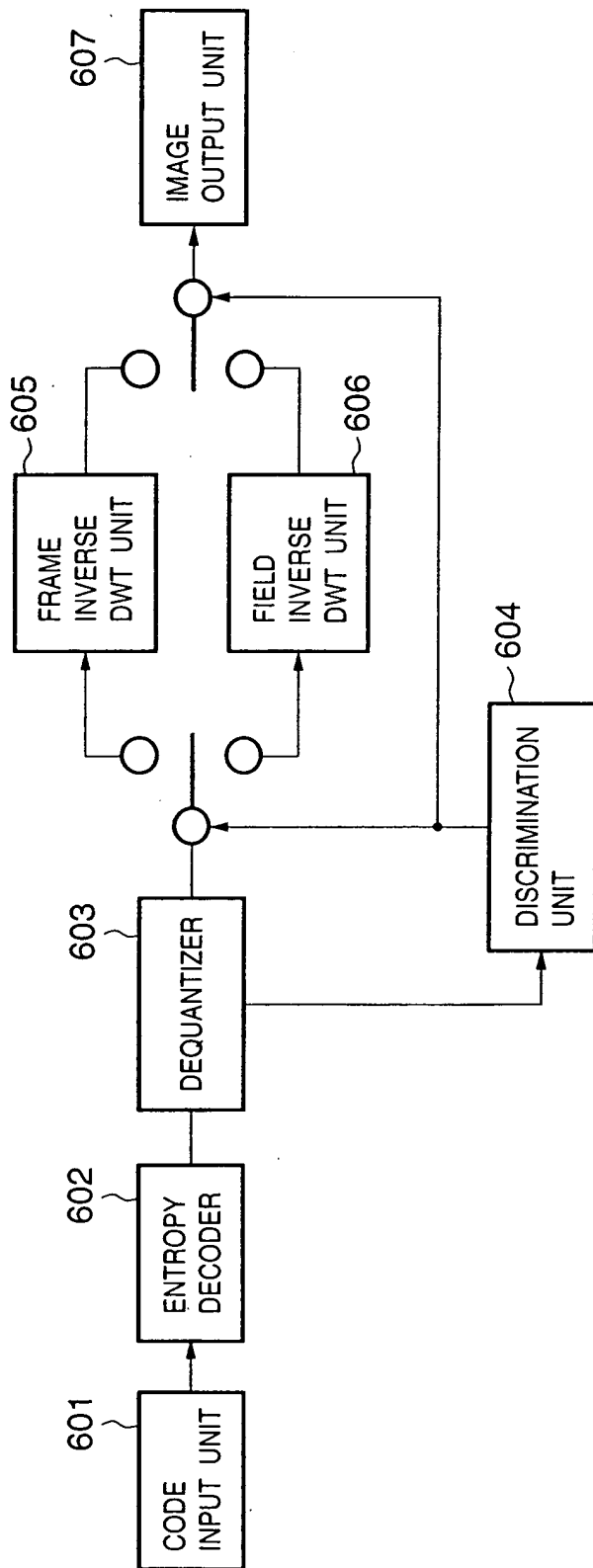


FIG. 6

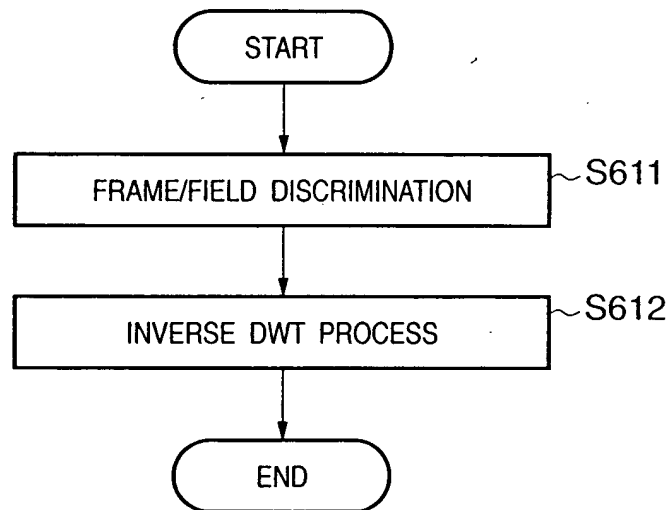


FIG. 7

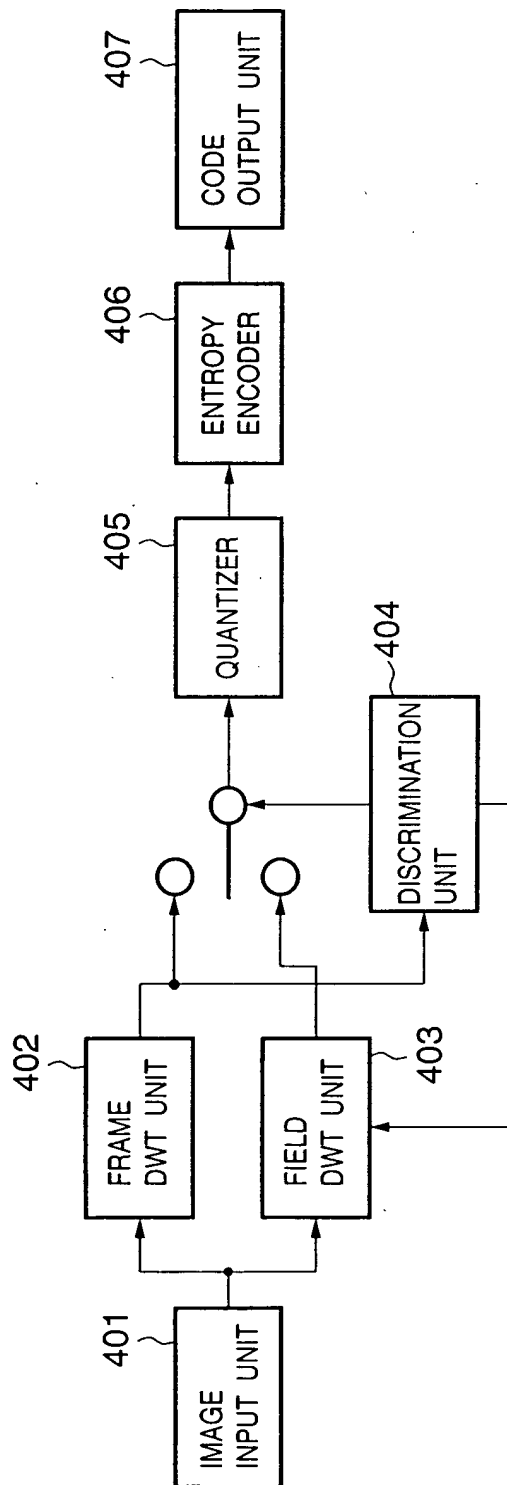


FIG. 8

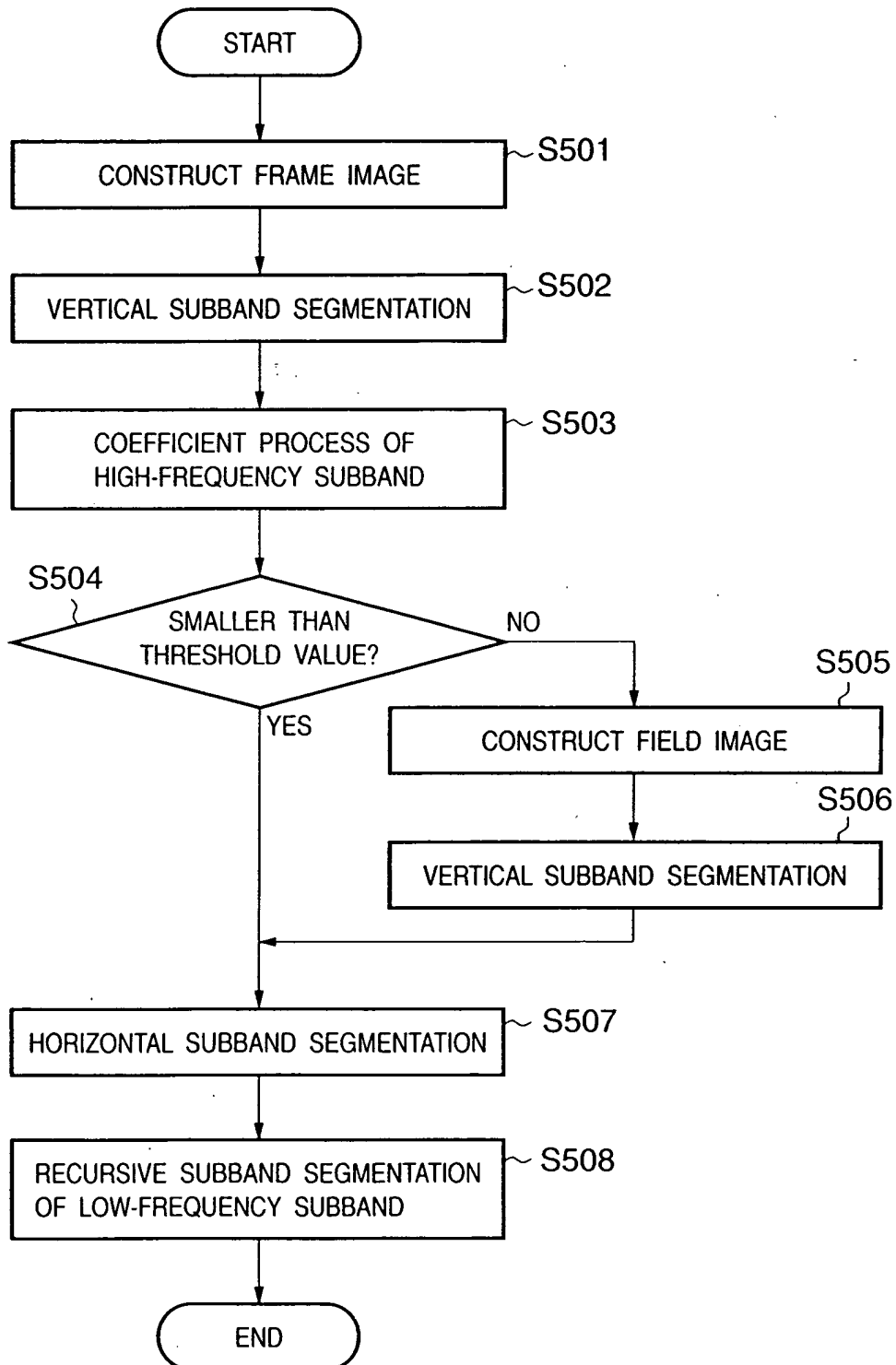


FIG. 9

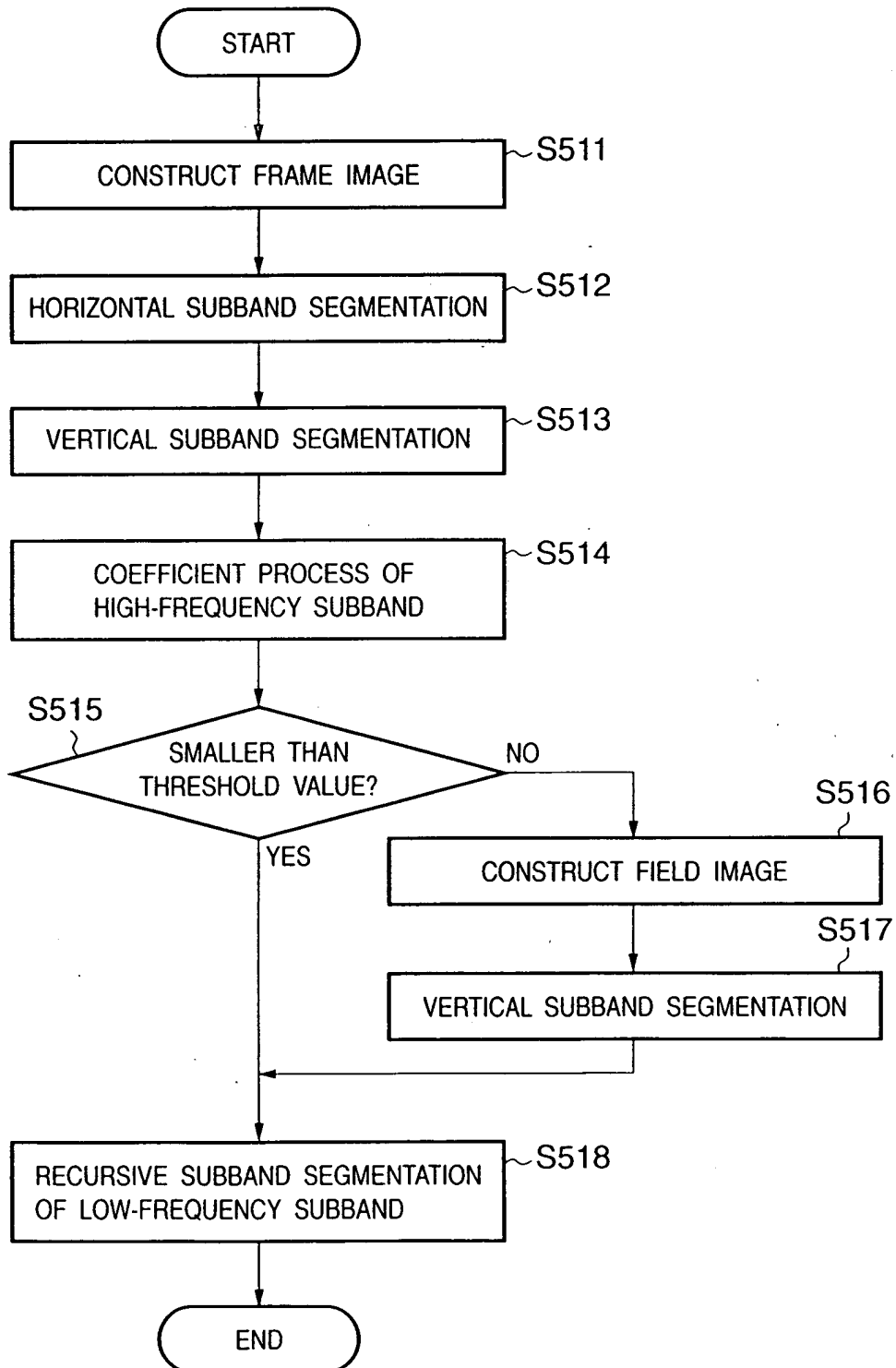


FIG. 10

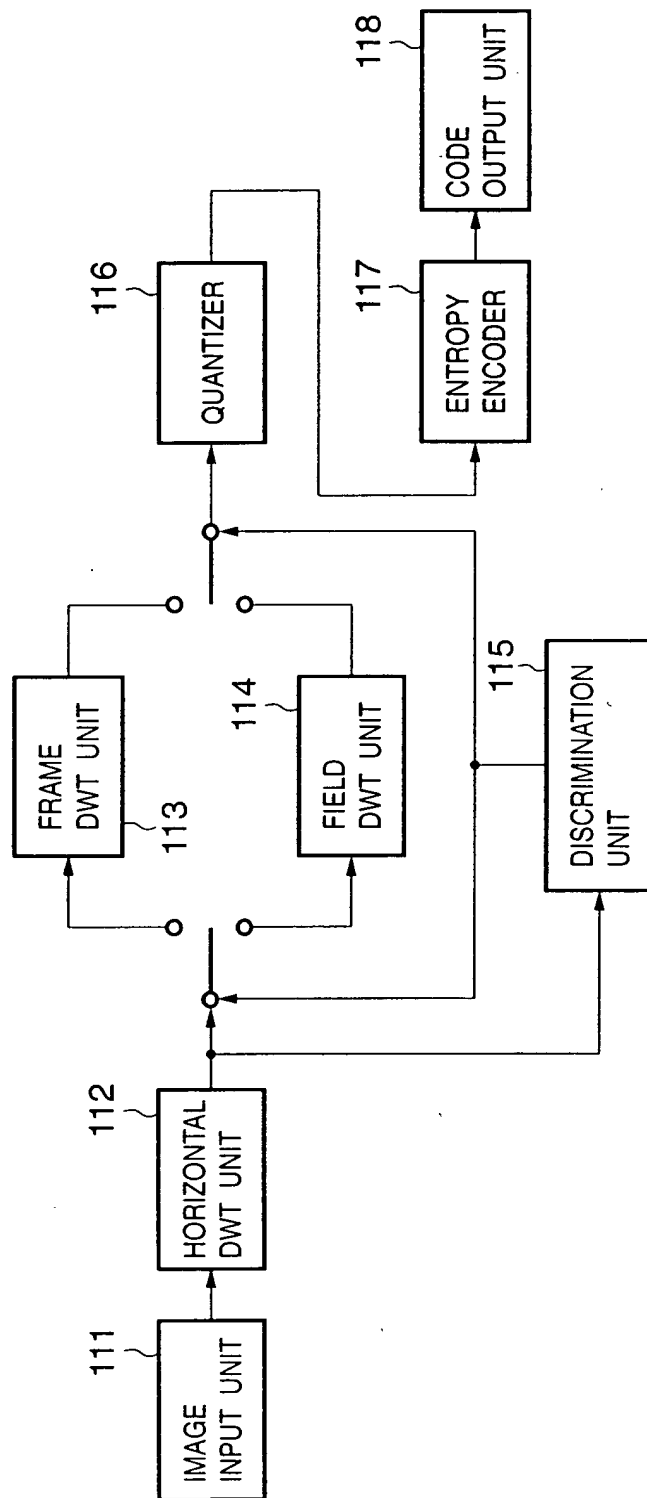


FIG. 11

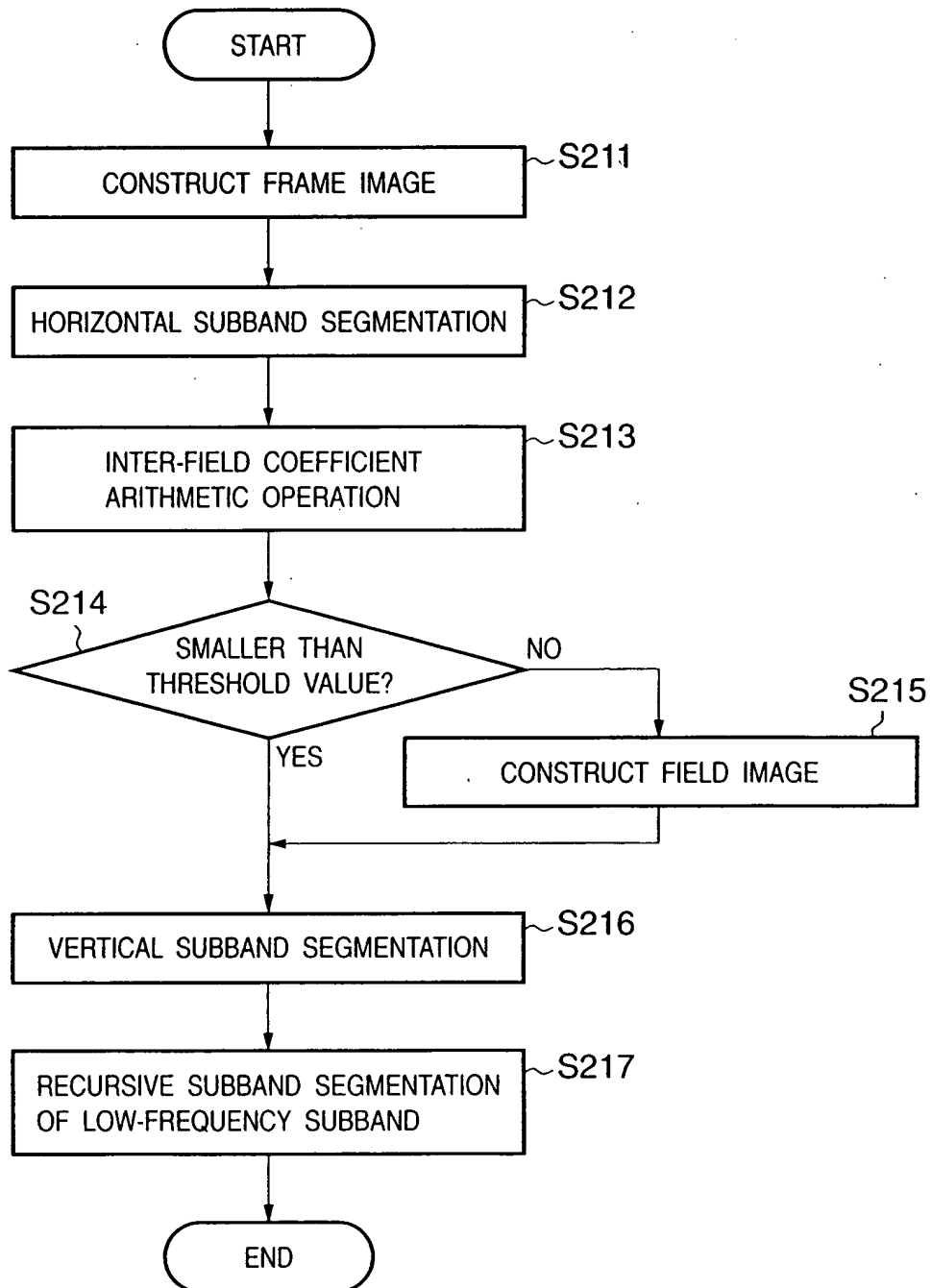


FIG. 12A

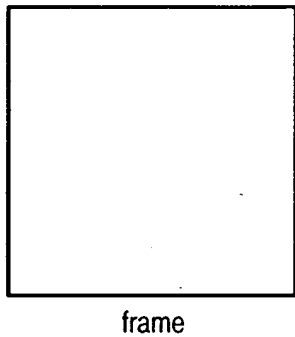


FIG. 12D

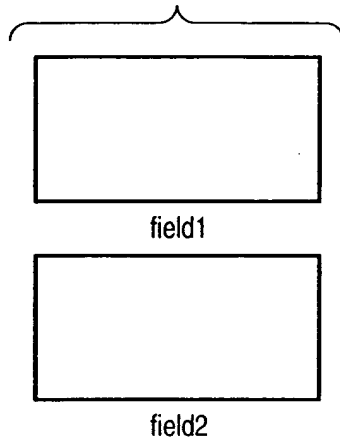


FIG. 12G

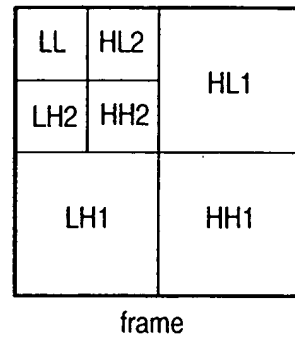


FIG. 12B

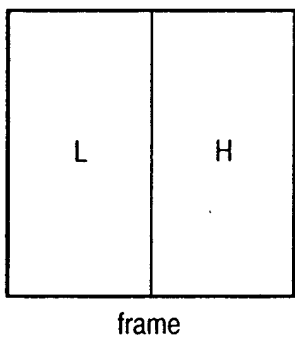


FIG. 12E

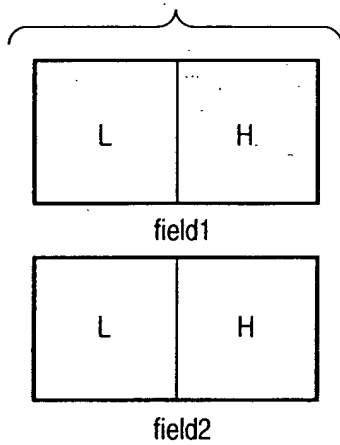


FIG. 12H

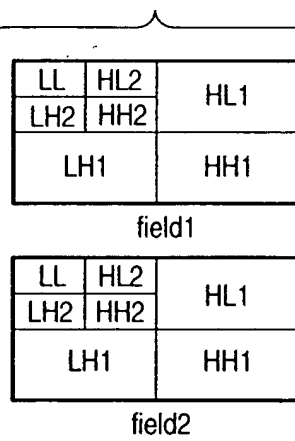


FIG. 12C

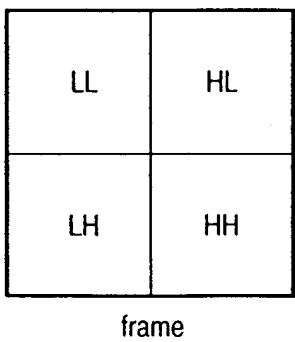


FIG. 12F

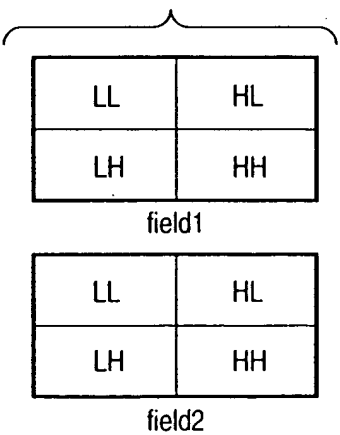


FIG. 13A

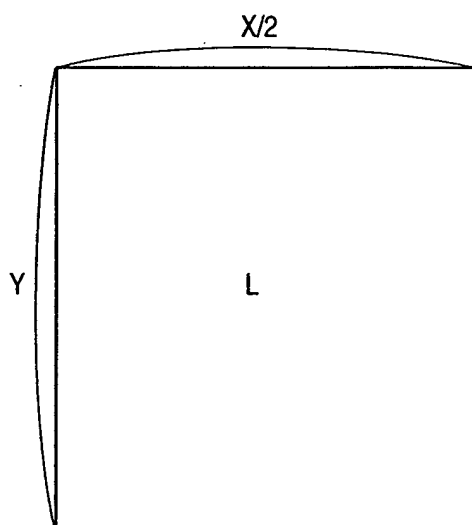


FIG. 13C

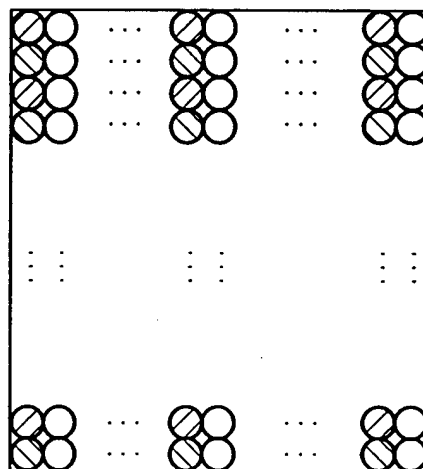


FIG. 13B

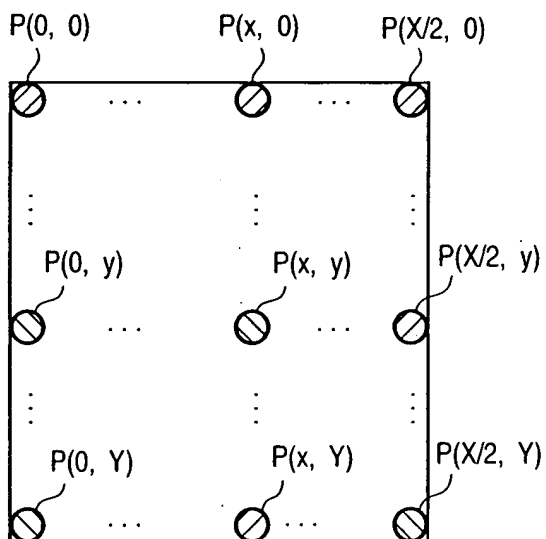


FIG. 13D

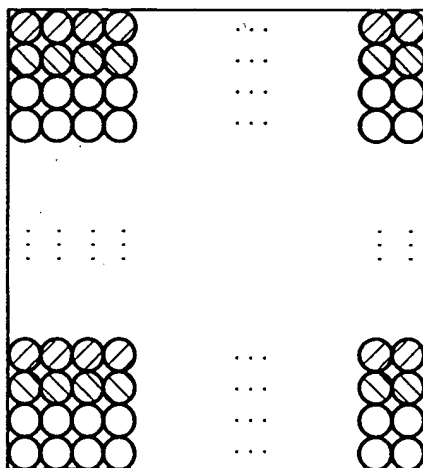


FIG. 14A

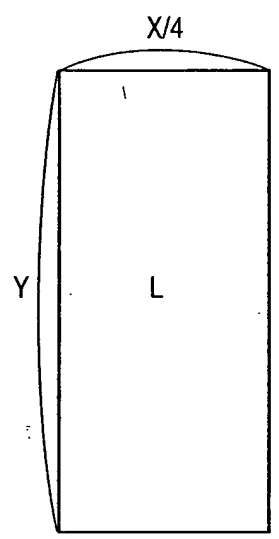


FIG. 14B

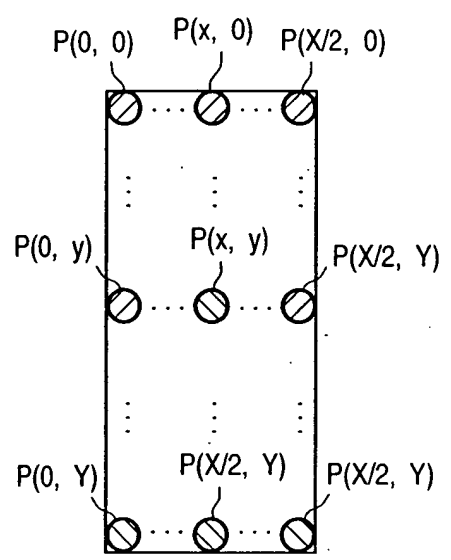


FIG. 14C

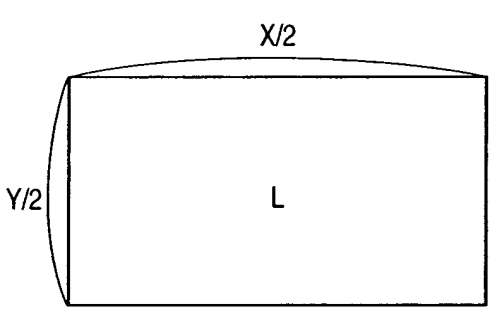


FIG. 14D

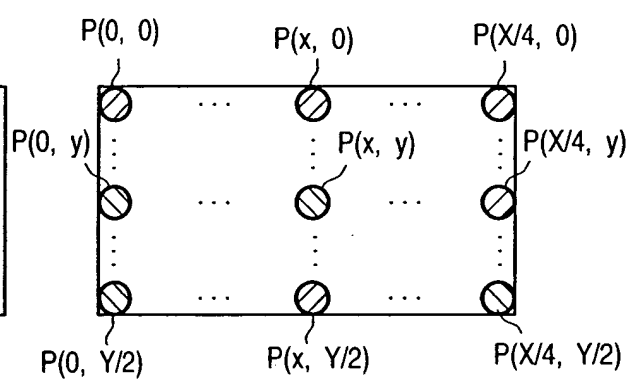


FIG. 14E

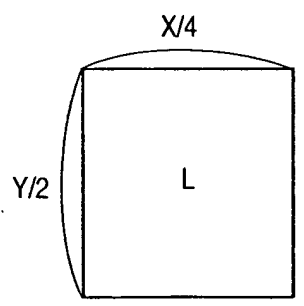


FIG. 14F

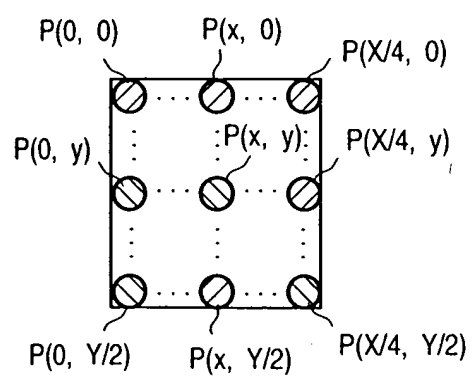


FIG. 15A

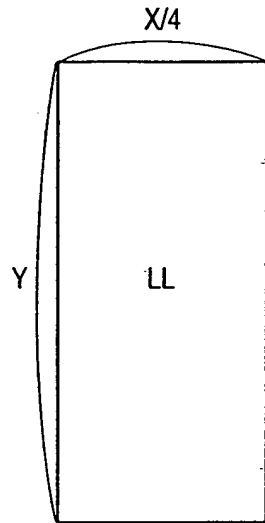


FIG. 15C

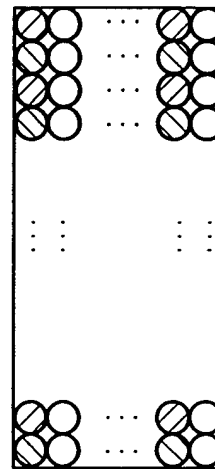


FIG. 15B

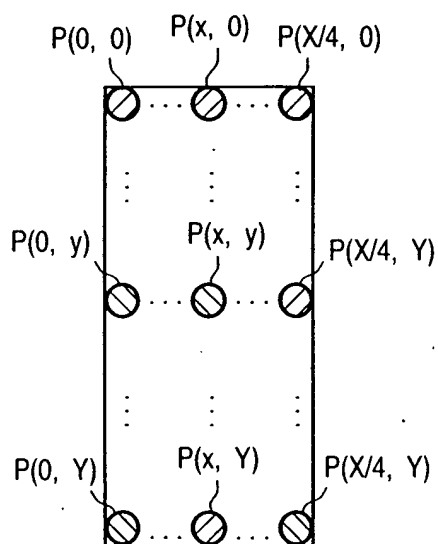


FIG. 15D

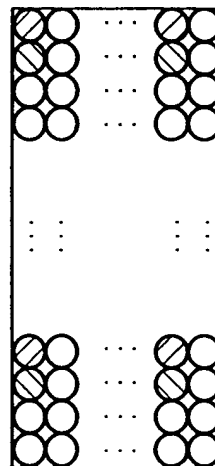


FIG. 16

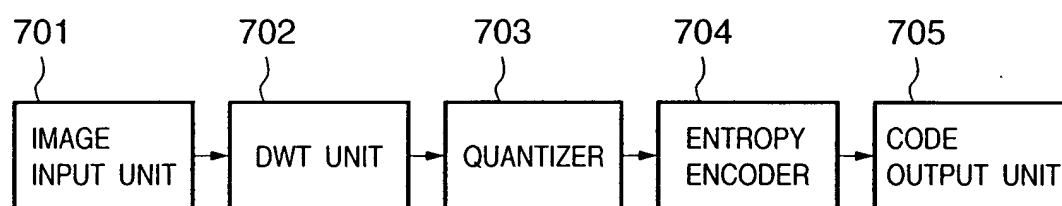


FIG. 17

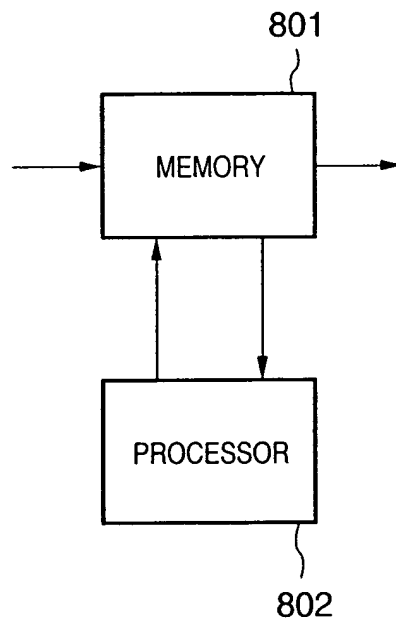
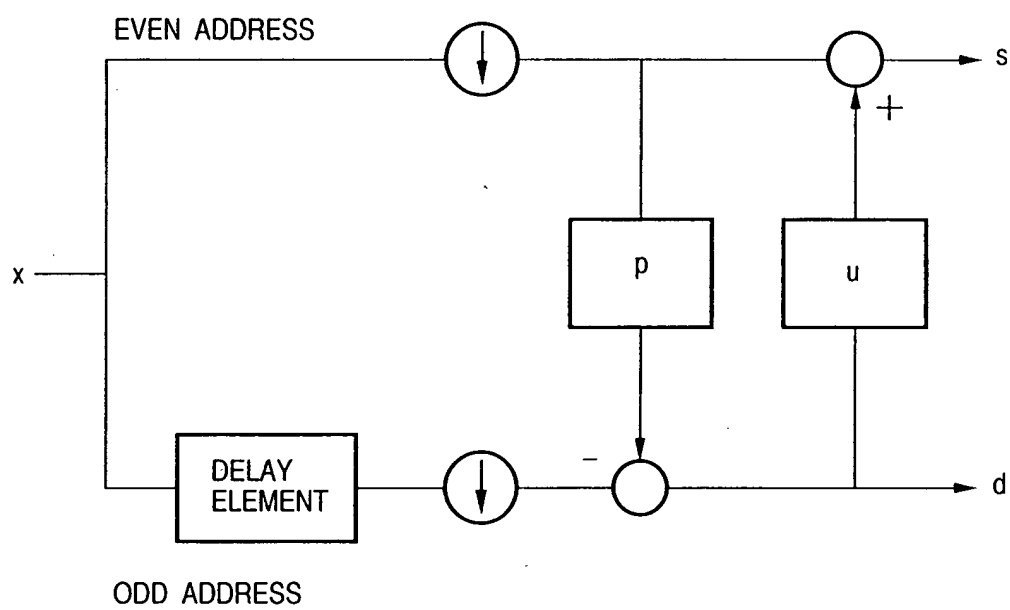


FIG. 18

| | | |
|-----|-----|-----|
| LL | HL2 | HL1 |
| LH2 | HH2 | |
| LH1 | | HH1 |

FIG. 20A

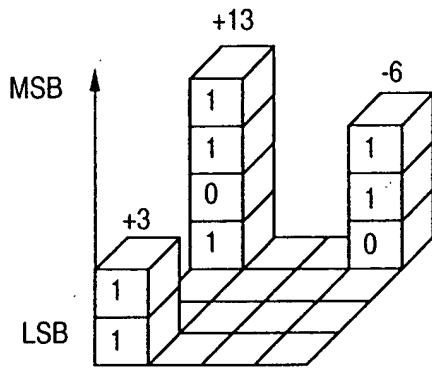


FIG. 20B

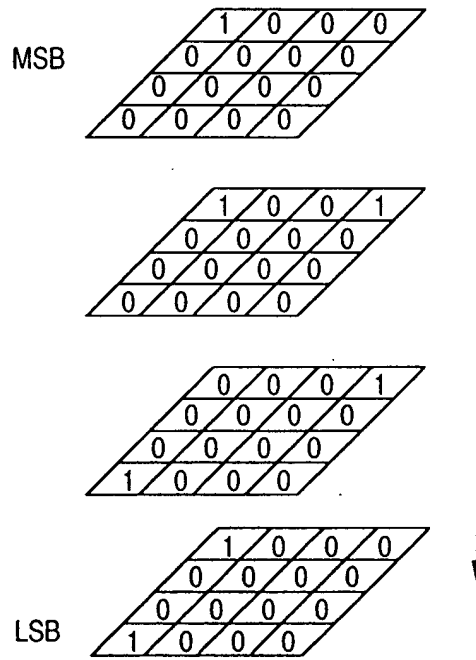


FIG. 21A

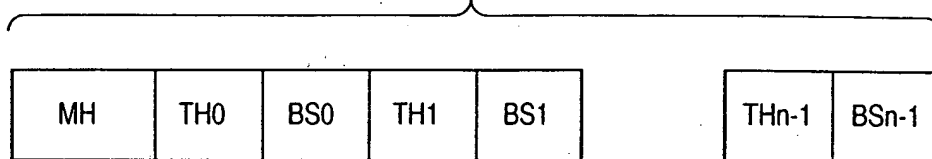


FIG. 21B

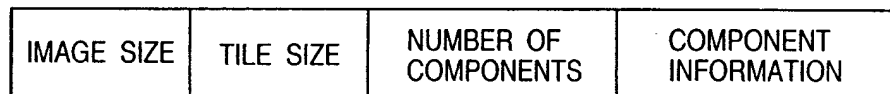


FIG. 21C

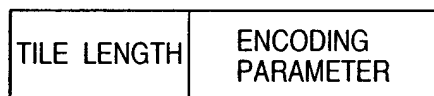
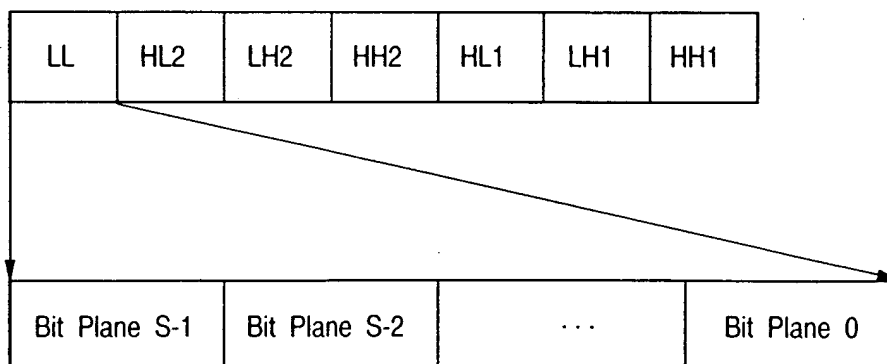


FIG. 21D



| | | | | | | | | |
|----|-----|-----|-----|-----|--|--|-------|-------|
| MH | TH0 | BS0 | TH1 | BS1 | | | THn-1 | BSn-1 |
|----|-----|-----|-----|-----|--|--|-------|-------|

| IMAGE SIZE | TILE SIZE | NUMBER OF COMPONENTS | COMPONENT INFORMATION |
|------------|-----------|----------------------|-----------------------|
|------------|-----------|----------------------|-----------------------|

| TILE LENGTH | ENCODING PARAMETER |
|-------------|-----------------------|
|-------------|-----------------------|

| Bit Plane S-1 | | | Bit Plane S-2 | | Bit Plane 0 | | | | | | |
|---------------|-----|-----|---------------|-----|-------------|-----|-----|-----|-----|-----|-----|
| LL | HL2 | LH2 | LL | ... | LL | HL2 | LH2 | HH2 | HL1 | LH1 | HH1 |

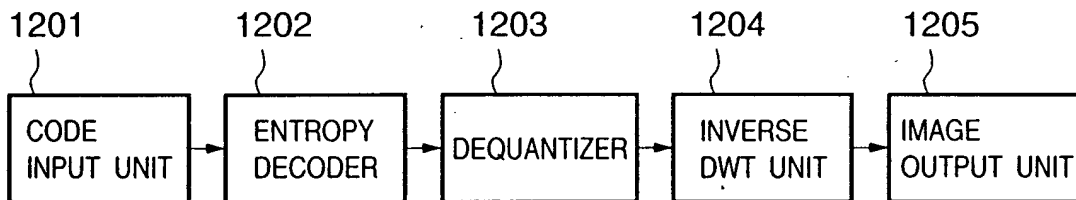
FIG. 23

FIG. 24A

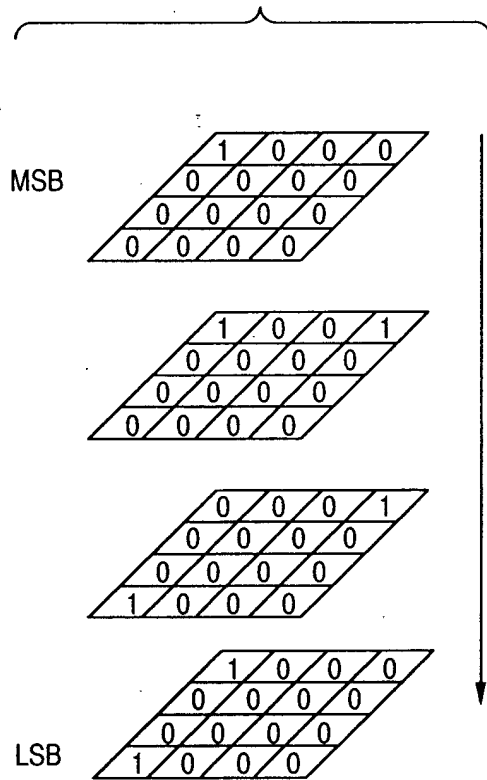


FIG. 24B

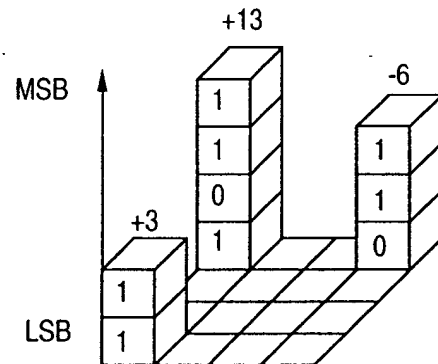


FIG. 25

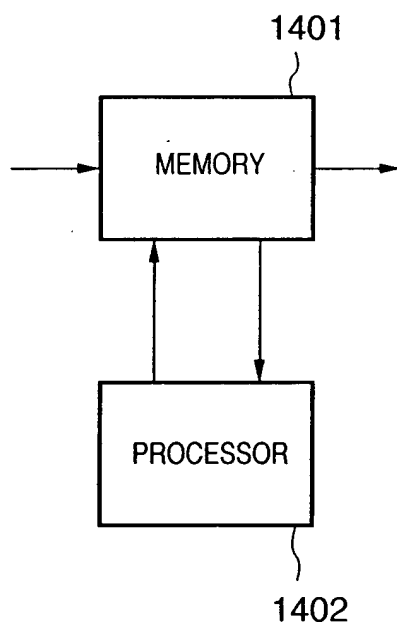


FIG. 26

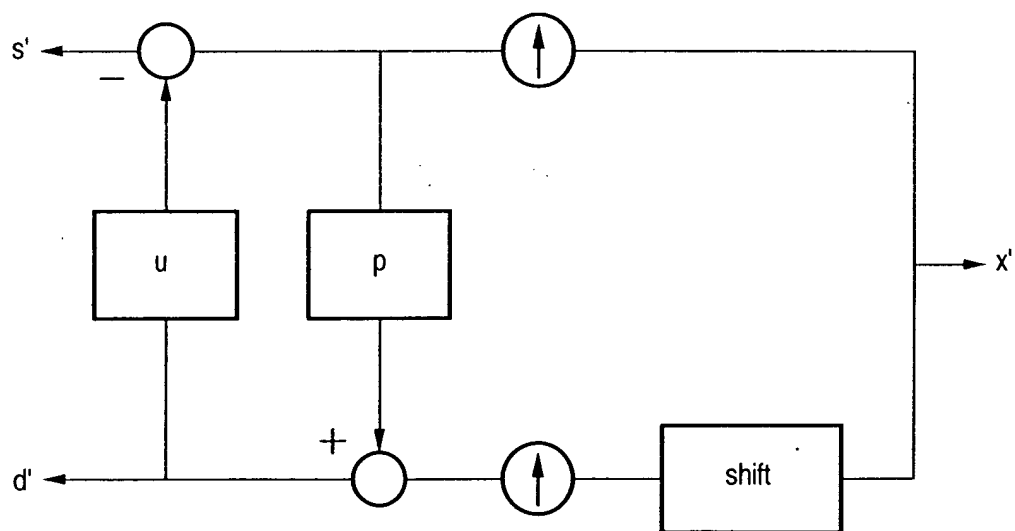


FIG. 27A

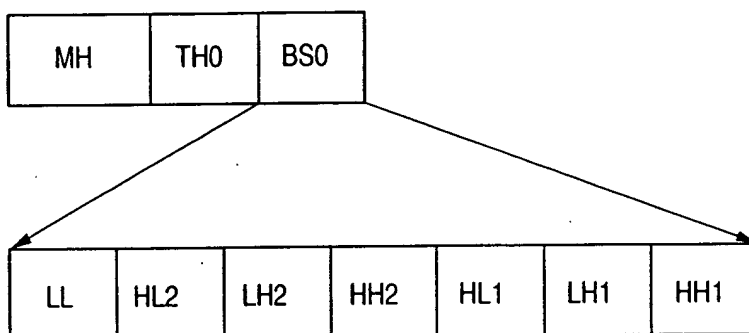


FIG. 27B

ABC

=LL

ABC

=LL+HL2+LH2+HH2

ABC

=LL+HL2+LH2+HH2+HL1+LH1+HH1

FIG. 28A

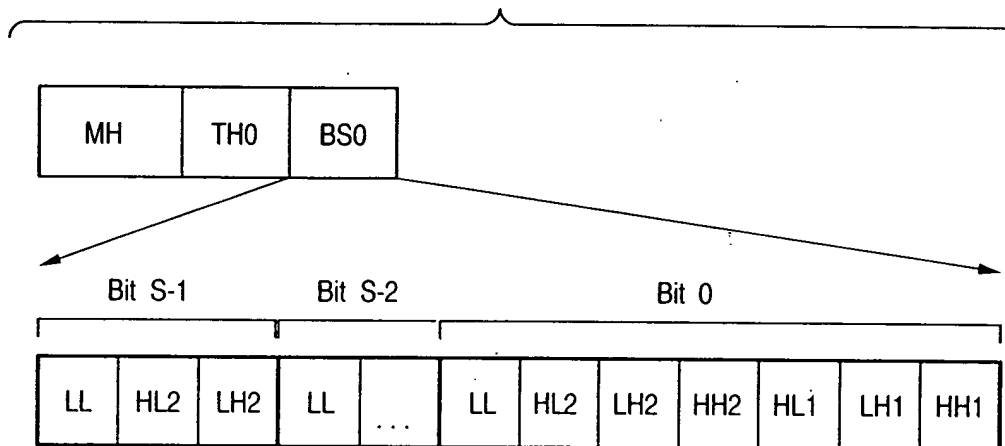


FIG. 28B

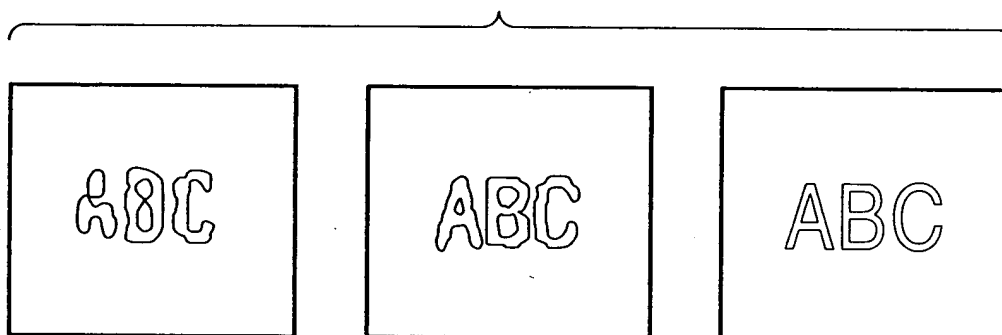


FIG. 29

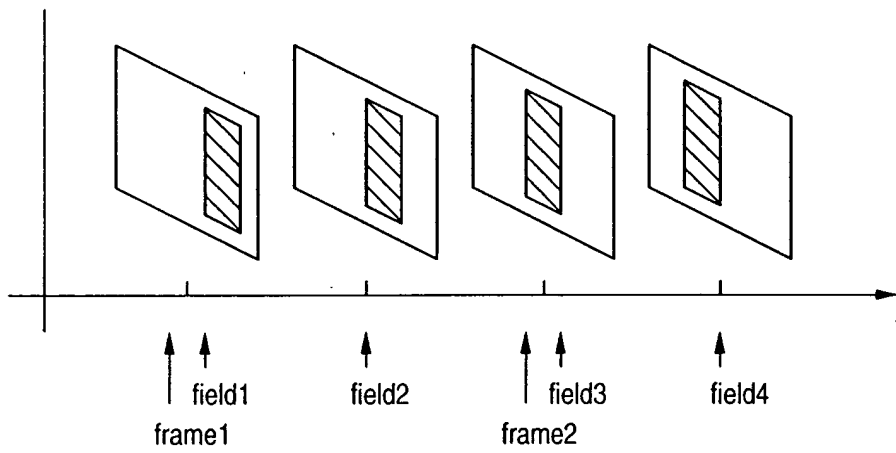


FIG. 30A

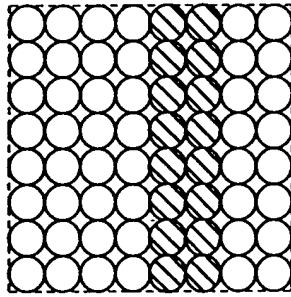


FIG. 30B

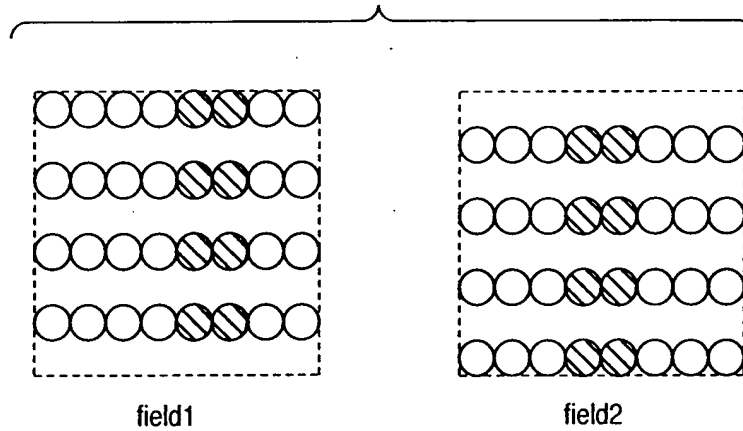


FIG. 30C

